

CLAIMS:

1. A method comprising:
  - 2 obtaining frequency estimation information from a first wireless signal received from a first carrier;
  - 4 performing a handover to a second carrier; and
  - 6 configuring a frequency tracking loop for receiving a second wireless signal from the second carrier as a function of the frequency estimation information.
2. The method of claim 1, wherein the frequency estimation information comprises a frequency offset.
3. The method of claim 1, wherein the first wireless signal is a CDMA signal and the second wireless signal is a GSM signal.
4. The method of claim 3, wherein the CDMA signal is one of a W-CDMA signal and a CDMA2000 signal.
5. The method of claim 1, wherein the first wireless signal is a GSM signal and the second wireless signal is a CDMA signal.
6. The method of claim 1, wherein the frequency tracking loop configures a voltage-controlled, temperature-compensated oscillator as a function of the frequency estimation information.
7. The method of claim 1, wherein the frequency tracking loop configures a rotator as a function of the frequency estimation information.
8. The method of claim 1, further comprising obtaining handover information during an allocated time slot.

9. The method of claim 8, wherein the handover information comprises at  
2 least one of received signal code power (RSCP), signal-to-interference ratio (SIR), and a  
received signal strength indicator (RSSI).

10. The method of claim 8, wherein the allocated time slot occurs during a  
2 compressed mode.

11. A processor readable medium containing processor executable instructions  
2 for:  
obtaining frequency estimation information from a first wireless signal received  
4 from a first carrier;  
performing a handover to a second carrier; and  
6 configuring a frequency tracking loop for receiving a second wireless signal  
from the second carrier as a function of the frequency estimation information.

12. The processor readable medium of claim 11, wherein the frequency  
2 estimation information comprises a frequency offset.

13. The processor readable medium of claim 11, wherein the first wireless  
2 signal is a CDMA signal.

14. The processor readable medium of claim 13, wherein the CDMA signal is  
2 one of a W-CDMA signal and a CDMA2000 signal.

15. The processor readable medium of claim 11, wherein the second wireless  
2 signal is a GSM signal.

16. The processor readable medium of claim 11, wherein the first wireless  
2 signal is a GSM signal and the second wireless signal is a CDMA signal.

2           17. The processor readable medium of claim 11, wherein the frequency  
tracking loop configures a voltage-controlled, temperature-compensated crystal oscillator  
4 as a function of the frequency estimation information.

2           18. The processor readable medium of claim 11, wherein the frequency  
tracking loop configures a rotator as a function of the frequency estimation information.

2           19. The processor readable medium of claim 11, further containing processor  
executable instructions for obtaining handover information during an allocated time slot.

2           20. The processor readable medium of claim 19, wherein the handover  
information comprises at least one of received signal code power (RSCP), signal-to-  
interference ratio (SIR), and a received signal strength indicator (RSSI).

2           21. The processor readable medium of claim 19, wherein the allocated time  
slot occurs during a compressed mode.

2           22. An apparatus comprising:  
a first receiver to receive a first signal from a first carrier, the first receiver  
comprising a first frequency tracking loop to obtain frequency estimation information  
4 relating to the first signal; and  
a second receiver to receive a second signal from a second carrier, the second  
6 receiver comprising a second frequency tracking loop to obtain frequency estimation  
information relating to the second signal as a function of the frequency estimation  
8 information relating to the first signal.

2           23. The apparatus of claim 22, wherein the first frequency tracking loop is  
configured to obtain the frequency estimation information relating to the first signal as a  
function of the frequency estimation information relating to the second signal.

2           24. The apparatus of claim 22, wherein at least one of the first and second  
frequency estimation information comprises a frequency offset.

2           25. The apparatus of claim 22, wherein at least one of the first and second  
frequency tracking loops configures a voltage-controlled, temperature-compensated  
crystal oscillator.

2           26. The apparatus of claim 22, wherein at least one of the first and second  
frequency tracking loops configures a rotator.

2           27. The apparatus of claim 22, wherein at least one of the first and second  
receivers is configured to obtain handover information during an allocated time slot.

2           28. The apparatus of claim 27, wherein the handover information comprises at  
least one of received signal code power (RSCP), signal-to-interference ratio (SIR), and a  
received signal strength indicator (RSSI).

2           29. The apparatus of claim 27, wherein the allocated time slot occurs during a  
compressed mode.

2           30. The apparatus of claim 22, wherein at least one of the first and second  
receivers comprises a RAKE receiver.

2           31. The apparatus of claim 22, wherein at least one of the first and second  
receivers comprises a GSM receiver.

2           32. An apparatus comprising:  
means for obtaining frequency estimation information from a first wireless  
signal received from a first carrier;  
4           means for performing a handover to a second carrier; and

means for configuring a frequency tracking loop for receiving a second wireless  
6 signal from the second carrier as a function of the frequency estimation information.

33. The apparatus of claim 32, wherein the frequency estimation information  
2 comprises a frequency offset.

34. The apparatus of claim 32, wherein the first wireless signal is a CDMA  
2 signal and the second wireless signal is a GSM signal.

35. The apparatus of claim 34, wherein the CDMA signal is one of a W-  
2 CDMA signal and a CDMA2000 signal.

36. The apparatus of claim 32, wherein the first wireless signal is a GSM  
2 signal and the second wireless signal is a CDMA signal.

37. The apparatus of claim 32, wherein the frequency tracking loop configures  
2 a voltage-controlled, temperature-compensated oscillator as a function of the frequency  
estimation information.

38. The apparatus of claim 32, wherein the frequency tracking loop configures  
2 a rotator as a function of the frequency estimation information.

39. The apparatus of claim 32, further comprising means for obtaining  
2 handover information during an allocated time slot.

40. The apparatus of claim 39, wherein the handover information comprises at  
2 least one of received signal code power (RSCP), signal-to-interference ratio (SIR), and a  
received signal strength indicator (RSSI).

41. The apparatus of claim 39, wherein the allocated time slot occurs during a  
2 compressed mode.